

PART B - FEE(S) TRANSMITTAL

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24309 7590 12/26/2006

XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

02/09/2007 FHETEK12 00000035 240040 10815492

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Pat Tompkins (Depositor's name)
Pat Tompkins (Signature)
February 9, 2007 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/815,492	04/01/2004	Tarek Eldin	X-1481 US	8518

TITLE OF INVENTION: METHOD OF AUTOMATIC FAULT ISOLATION IN A PROGRAMMABLE LOGIC DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$0	\$1400	03/26/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
SIDDIQUI, SAQIB JAVAID	2138	714-725000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 Scott Hewett

2

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

- ☒ Issue Fee
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☐ Advance Order - # of Copies

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☒ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Michael R. Hardaway
Typed or printed name Michael R. Hardaway

Date February 9, 2007

Registration No. 52,992

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Inventor(s): Tarek Eldin et al.

Assignee: XILINX, INC.

Serial No.: 10/815,492

conf.no. 8518

Filed: April 1, 2004

Title: Method of Automatic Fault Isolation in a Programmable Logic Device

Docket No.: X-1481 US

Enclosed: Return Receipt Postcard
Fee Transmittal Form - Part B

Date: February 9, 2007

VIA FACSIMILE 571-273-2885

Atty/Sec: MRH/pat